

LISTING OF CLAIMS

Please **amend** the claims as follows:

1. (Canceled)

2. (Canceled)

3. (Currently amended) A display device comprising:

a first substrate;

a gate line formed on the first substrate;

a data line intersected with the gate line;

a second substrate facing the first substrate; and

a plurality of column spacers formed on at least one of the first and the second substrate and located in a display area,

wherein the spacers comprise a first spacer and a second spacer, and contact areas of the first and the second spacer are different, and at least one of the spacers has a tapered structure.

4. (Previously presented) The display device of claim 3, wherein a height difference between the first and the second spacer is in a range of about 0.3-0.6 microns.

5. (Previously presented) The display device of claim 3, wherein a height of the second spacer is lower than the first spacer .

6. (Previously presented) The display device of claim 3, wherein the second spacer has a length in a range of about 30-35 microns and the first spacer has a length in a range of about 15-20 microns.

7. (Previously presented) The display device of claim 3, wherein a concentration of the second spacers is about 200-600/cm² and a concentration of the first spacer is about 250-450/cm².

Claims 8-12 (Canceled).

13. (Original) A method of manufacturing a liquid crystal panel assembly, the method comprising:

coating a photoresist on a panel;

light-exposing the photoresist through an exposure mask including an opening and disposed on the panel with a first distance;

light-exposing the photoresist through the exposure mask disposed on the panel with a second distance; and

developing the photoresist to form first and second spacers having different heights or different contact areas with the panel.

14. (Original) The method of claim 13, wherein the photoresist is a negative type.

Claims 15-21 (Canceled).

22. (Currently amended) A display device, comprising:

a first substrate;

a gate line formed on the first substrate;

a data line intersected with the gate line;

a second substrate facing the first substrate;

a blocking layer formed on at least one of the first and second substrate; and

a plurality of column spacers formed on at least one of the first and the second substrate and located in a display area.

wherein the spacers comprise a first spacer and a second spacer, and contact areas of the first and second spacers are different, and at least a portion of the spacers is overlapped with the blocking layer.

23. (Previously presented) The display device of claim 22, wherein a height of the second spacer is lower than a height of the first spacer.

24. (Previously presented) The display device of claim 23, wherein a height difference between the first and the second spacer is in a range of about 0.3-0.6 microns.

25. (Previously presented) The display device of claim 22, wherein a length difference between the first and the second spacer is in a range of about 10-20 microns.

26. (Previously presented) The display device of claim 25, wherein the second spacer has a length in a range of about 30-35 microns and the first spacer has a length in a range of about 15-20 microns.

27. (Previously presented) The display device of claim 22, wherein a concentration of the second spacer is about $200\text{-}600/\text{cm}^2$ and a concentration of the first spacer is about $250\text{-}450/\text{cm}^2$.

28. (Previously presented) The display device of claim 22, wherein the spacers further comprise a third spacer, and the second spacer has a height lower than a height of the first spacer and the third spacer has a height equal to or lower than the height of the second spacer.

29. (Previously presented) The display device of claim 28, wherein the height of the third spacer is equal to the height of the second spacer.

30. (Previously presented) The display device of claim 22, wherein the first substrate comprises a thin film transistor electrically connected to the gate line and the data line, and a pixel electrode connected to the thin film transistor.

31. (Previously presented) The display device of claim 22, wherein the second substrate comprises a plurality of color filters having different thicknesses.

32. (Previously presented) The display device of claim 3, wherein the second spacer has a length larger than the first spacer by 10-20 microns.

33. (Previously presented) The display device of claim 22, wherein at least one of the spacers has a tapered structure.

34. (Currently amended) The display device of claim 30, wherein at least a portion of at least one of the spacers ~~space~~ is overlapped with at least a portion of the data line.

35. (Currently amended) A display device comprising:
a first substrate;
a gate line formed on the first substrate;
a data line intersected with the gate line;
a second substrate facing the first substrate;
a blocking layer formed on at least one of the first and the second substrate; and
a plurality of column spacers formed on at least one of the first and the second substrate and located in a display area,

wherein the spacers comprise a first spacer and a second spacer, and heights of the first and the second spacer are different, and at least a portion of the spacers is overlapped with the blocking layer.